

IN THE CLAIMS:

Please amend the claims as follows:

Sub D1
1. (Twice Amended) A display device comprising:

a pixel portion in which ($m \times 2n$) pixels are arranged in matrix form (both m and n are natural numbers);

a source driver for supplying video signals to $2n$ source signal lines $S_1, S_2, \dots, S_n, S_{n+1}, S_{n+2}, \dots, S_{2n}$;

a first gate driver for supplying selection signals to m first gate signal lines $G_{1L}, G_{2L}, \dots, G_{mL}$; and

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a second gate driver for supplying selection signals to m second gate signal lines $G_{1R}, G_{2R}, \dots, G_{mR}$, wherein:

the pixels connected to the source signal lines S_1, S_2, \dots, S_n are supplied with the selection signals from the first gate signal lines $G_{1L}, G_{2L}, \dots, G_{mL}$;

the pixels connected to the source signal lines $S_{n+1}, S_{n+2}, \dots, S_{2n}$ are supplied with the selection signals from the second gate signal lines $G_{1R}, G_{2R}, \dots, G_{mR}$;

the selection signal starts to be supplied to the second gate signal line G_{1R} while the selection signal is supplied to the first gate signal line G_{1L} ; and

the selection signal starts to be supplied to the first gate signal line G_{2L} while the selection signal is supplied to the second gate signal line G_{1R} ,

wherein each of the pixel portion, the source driver, the first gate driver and the second gate driver comprises at least one TFT formed over a same substrate.

Sub D1
2. (Amended) A display device comprising:

a pixel portion in which ($m \times 2n$) pixels are arranged in matrix form (both m and n are natural numbers);

a source driver for supplying video signals to $2n$ source signal lines $S_1, S_2, \dots, S_n, S_{n+1}, S_{n+2}, \dots, S_{2n}$;

a first gate driver for supplying selection signals to m first gate signal lines $G_{1L}, G_{2L}, \dots, G_{mL}$; and

CG a second gate driver for supplying selection signals to m second gate signal lines $G_{1R}, G_{2R}, \dots, G_{mR}$, wherein:

the pixels connected to the source signal lines S_1, S_2, \dots, S_n are supplied with the selection signals from the first gate signal lines $G_{1L}, G_{2L}, \dots, G_{mL}$;

the pixels connected to the source signal lines $S_{n+1}, S_{n+2}, \dots, S_{2n}$ are supplied with the selection signals from the second gate signal lines $G_{1R}, G_{2R}, \dots, G_{mR}$; and

the selection signals are sequentially supplied to the first gate signal line G_{1L} , the second gate signal line G_{1R} , the first gate signal line G_{2L} , the second gate signal line G_{2R}, \dots , the first gate signal line G_{mL} , and the second gate signal line G_{mR} in this order with a delay of a half period between the respective adjacent gate signal lines,

wherein each of the pixel portion, the source driver, the first gate driver and the second gate driver comprises at least one TFT formed over a same substrate.

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23. (Amended) A method of driving an active matrix display device comprising:

a pixel portion in which ($m \times 2n$) pixels are arranged in matrix form (both m and n are natural numbers);

a source driver for supplying video signals to $2n$ source signal lines $S_1, S_2, \dots, S_n, S_{n+1}, S_{n+2}, \dots, S_{2n}$;

a first gate driver for supplying selection signals to m first gate signal lines $G_{1L}, G_{2L}, \dots, G_{mL}$; and

a second gate driver for supplying selection signals to m second gate signal lines $G_{1R}, G_{2R}, \dots, G_{mR}$, wherein said method comprises the steps of:

supplying the pixels connected to the source signal lines S_1, S_2, \dots, S_n with the selection signals from the first gate signal lines $G_{1L}, G_{2L}, \dots, G_{mL}$;

supplying the pixels connected to the source signal lines $S_{n+1}, S_{n+2}, \dots, S_{2n}$ with the selection signals from the second gate signal lines $G_{1R}, G_{2R}, \dots, G_{mR}$;

starting to supply the selection signal to the second gate signal line G_{1R} while the selection signal is supplied to the first gate signal line G_{1L} ; and

starting to supply the selection signal to the first gate signal line G_{1L} while the section signal is supplied to the second gate signal line G_{1R} ,

wherein each of the pixel portion, the source driver, the first gate driver and the second gate driver comprises at least one TFT formed over a same substrate.

24. (Twice Amended) A method of driving an active matrix display device comprising:

a pixel portion in which $(m \times 2n)$ pixels are arranged in matrix form (both m and n are natural numbers);

a source driver for supplying video signals to $2n$ source signal lines $S_1, S_2, \dots, S_n, S_{n+1}, S_{n+2}, \dots, S_{2n}$;

a first gate driver for supplying selection signals to m first gate signal lines $G_{1L}, G_{2L}, \dots,$

GmL; and

a second gate driver for supplying selection signals to m second gate signal lines G1R, G2R, . . . , GmR, wherein said method comprises the steps of:

supplying the pixels connected to the source signal lines S1, S2, . . . , Sn with the selection signals from the first gate lines G1L, G2L, . . . , GmL;

supplying the pixels connected to the source signal lines Sn+1, Sn+2, . . . , S2n with the selection signals from the second gate lines G1R, G2R, . . . , GmR; and

sequentially supplying the selection signals to the first gate signal line G1L, the second gate signal line G1R, the first gate signal line G2L, the second gate signal line G2R, . . . , the first gate signal line GmL, and the second gate signal line GmR in this order with a delay of a half period between the respective adjacent gate signal lines,

wherein each of the pixel portion, the source driver, the first gate driver and the second gate driver comprises at least one TFT formed over a same substrate.

25. (Amended) A display device comprising:

a pixel portion in which (m x 2n) pixels are arranged in matrix form (both m and n are natural numbers);

a source driver for supplying video signals to 2n source signal lines S1, S2, . . . , Sn, Sn+1, Sn+2, . . . , S2n;

a first gate driver for supplying selection signals to m first gate signal lines G1L, G2L, . . . , GmL; and

a second gate driver for supplying selection signals to m second gate signal lines G1R, G2R, . . . , GmR, wherein:

the pixels connected to the source signal lines S_1, S_2, \dots, S_n are supplied with the selection signals from the first gate signal lines $G_{1L}, G_{2L}, \dots, G_{mL}$;

the pixels connected to the source signal lines $S_{n+1}, S_{n+2}, \dots, S_{2n}$ are supplied with the selection signals from the second gate signal lines $G_{1R}, G_{2R}, \dots, G_{mR}$;

the selection signal starts to be supplied to the second gate signal line G_{1R} while the selection signal is supplied to the first gate signal line G_{1L} ; and

the selection signal starts to be supplied to the first gate signal line G_{2L} while the selection signal is supplied to the second gate signal line G_{1R} ,

wherein the m first gate signal lines $G_{1L}, G_{2L}, \dots, G_{mL}$ of the first gate driver are not connected to the m second gate signal lines $G_{1R}, G_{2R}, \dots, G_{mR}$ of the second gate driver, and

wherein each of the pixel portion, the source driver, the first gate driver and the second gate driver comprises at least one TFT formed over a same substrate.

26. (Amended) A display device comprising:

a pixel portion in which $(m \times 2n)$ pixels are arranged in matrix form (both m and n are natural numbers);

a source driver for supplying video signals to $2n$ source signal lines $S_1, S_2, \dots, S_n, S_{n+1}, S_{n+2}, \dots, S_{2n}$;

a first gate driver for supplying selection signals to m first gate signal lines $G_{1L}, G_{2L}, \dots, G_{mL}$; and

a second gate driver for supplying selection signals to m second gate signal lines $G_{1R}, G_{2R}, \dots, G_{mR}$, wherein:

the pixels connected to the source signal lines S_1, S_2, \dots, S_n are supplied with the selection

signals from the first gate signal lines $G1L, G2L, \dots, GmL$;

the pixels connected to the source signal lines $S_{n+1}, S_{n+2}, \dots, S_{2n}$ are supplied with the selection signals from the second gate signal lines $G1R, G2R, \dots, GmR$;

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cancel.* the selection signal starts to be supplied to one of the i -th gate signal line GiL and the second gate signal line GiR while the selection signal is supplied to the other one of the first gate signal line GiL and the second gate signal line GiR ,

wherein each of the pixel portion, the source driver, the first gate driver and the second gate driver comprises at least one TFT formed over a same substrate.

~~Cancel Claim 27.~~